



OPEGC 2009

Invitation

Centre for Development of Advanced Computing (C-DAC)
&

Indian Institute of Technology (IIT) Madras, Chennai
cordially invites you
to the inauguration of

Technology Workshop on

Optimizing Performance of Parallel Programs on Emerging Multi-Core Processors & GPUs

Inauguration Ceremony

at 9:30 am, June 1, 2009

Venue :

Media Resource Centre, Central Library,
Indian Institute of Technology Madras.

- 9.30 am **Welcome Address**
Prof. M. S. Shunmugam
Head -Dept. of Mechanical Engg., IIT Madras
- 9.35 am **Inauguration**
Prof. S. Santhakumar
Dean, Academic Courses, IIT Madras
- 9.40 am **About the workshop.**
Dr. V.C.V Rao C-DAC, Pune
- 9.50 am **C-DAC: Role of High Performance Computing**
Shri M. R. Rajagopalan
Director, C-DAC Chennai
- 10.10 am **Emerging Parallel Processing Platform
Multi-core Processors & GPUs**
Prof. R. Kalyana Krishnan
Dept. of Comp. Science & Engg., IIT Madras
- 10.30 am **Vote of Thanks**
Dr. N. Siva Prasad
Dept. of Mechanical Engg., IIT Madras
- All are cordially invited**

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