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January 10, 2020

Exascale Computing Symposium

(A C-DAC 'HPC-AI Convergence Year' Event)

Innovation
Through
Acceleration



Venue: Centre for Development of Advanced Computing (C-DAC), Innovation Park, Panchavati, Pashan, Pune - 411008.

PROGRAMME

January 10, 2020

9.30 am to 5.30 pm

C-DAC Innovation Park, Panchavati, Pashan, Pune - 411008

Time	Sessions	Speakers
09:30 – 09:40	Inaugural Session	Welcome and agenda briefing Vishal Dhupar , Managing Director, NVIDIA - India & South Asia
09:40 – 09:50		Keynote address by Dr. Hemant Darbari , Director General, CDAC
09:50 – 10:00		Address by Guest of Honour Dr. K D Nayak , Former Director General MED,COS & CS and Member, NSM, TAC
10:00 – 10:15		Inaugural address by Chief Guest Dr. Vijay P Bhatkar , Chairman, C-DAC TAC and Chancellor, Nalanda University
10:15 – 10:20		Vote of Thanks by Col. Asheet Nath (Retd.) , Executive Director, C-DAC Pune
10:20 – 10:30	Tea/Coffee Break	
Session I: Vision for Exascale Computing with Accelerators		
Session Coordinator: Marc Hamilton, VP Solutions Architecture, NVIDIA & Dr Rajendra Joshi, Senior Director, HPC-MBA, C-DAC, Pune		
10:30 – 11:00	Planning for Exascale	Giridhar Chukkapalli - CTO, Accelerated Computing, NVIDIA
11:00 – 11:30	Exascale Architectural Roadmap	Marc Hamilton, VP Solutions Architecture, NVIDIA
11:30 – 12:00	HPC Applications for Exascale	Dr Rajendra Joshi, Senior Director, HPC-MBA, C-DAC, Pune
12:00 – 12:30	HPC/AI Convergence to Exascale	Fred Allman, Director WW Supercomputing, NVIDIA
12:30 – 13:00	Space Applications for Exascale	Dr. V. Ashok, Dy. Director - VSSC AERO, Dept. of Space, GoI
13:00 – 13:30	Exascale Application Readiness & ARM+GPU Update	CJ Newburn, Principal Architect & Chief Strategist, HPC Software, NVIDIA
13:30 – 14:30	Lunch Break	
Session II: Implementation of Vision and Roadmap		
Session Coordinator: Tim Costa, Product Manager HPC Software, NVIDIA & Sanjay Wandhekar, Senior Director, HPC-Tech, C-DAC, Pune		
14:30 – 15:00	Software Architecture and CUDA-X & Libraries for Exascale	Tim Costa, Product Manager HPC Software, NVIDIA
15:00 – 15:30	Interconnect for Exascale	Richard Graham, Sr. HPC Technologist, MELLANOX
15:30 – 16:00	Atmospheric R&D for Exascale	Dr Amit Kesarkar, Scientist & Head WCRG, NARL, DoS, GoI
16:00 – 16:30	Oceanic Applications for Exascale	Dr. Suresh I, Principal Scientist, CSIR-NIO, Goa
16:30 – 16:45	Tea/Coffee Break	
Session III: Panel Discussion - India's readiness and path for Exascale		
Session Moderator: Shri S A Kumar, Director, HPC, MeitY, Govt. of India & Abhishek Das, Joint Director, (HPC-Technologies), C-DAC, Pune		
16:45 – 17:30	Prof. Sashikumar Ganeshan, Chairman, Dept. of CDS & Head, CMG, IISc, Bangalore Prof. S M Deshpande, Sr. Research Associate, EMU, JNCASR, Bangalore Mr Atul Bodas, Senior Director, HPC Technologies, C-DAC, Pune Dr Manish Agarwal, System Architect, CSE, IIT Delhi Prof Sharad Sinha, CSE, IIT Goa Mr Goldi Misra, CTO, IISER, Pune	