

Category No.1: C-DAC, Bengaluru, Project Engineer – VLSI Design (Logic Design)

Post:	Project Engineer – VLSI Design (Logic Design)
Post Code:	PE - 01/LD
No. of Posts	4
Duration of the position	Two Years – Extendable based on performance and availability of projects
Educational Qualification	<ul style="list-style-type: none">• Full time 1st Class B.E/B. Tech in Electronics & Communication /Telecommunication/Electronics or equivalent with 1 to 3 years of relevant post qualification work experience (Candidates who have passed out UG in the year 2014 or later are ONLY eligible to apply) <p style="text-align: center;">OR</p> <ul style="list-style-type: none">• Full time 1st Class B.E/B. Tech in Electronics & Communication/ Telecommunication/Electronics or equivalent (Candidates who have passed out UG in the year 2013 or later are ONLY eligible to apply) Full time 1st class M.E./M. Tech in Micro-Electronics/VLSI Design/Electronics or equivalent plus at least 1 year of relevant post qualification work experience (Candidates who have passed out PG in the year 2015 or later are ONLY eligible to apply)
Mandatory Experience (in one or more topics)	<ul style="list-style-type: none">• Hands-on experience in RTL simulation, verification and modeling using VHDL/Verilog/System Verilog• Knowledge of Digital IC Design (Simulation/Synthesis [ASIC/FPGA]/Static Timing Analysis/Logic Equivalence Check/Back-Annotation/DFT)• Experience in prototyping the developed IP cores on FPGAs• Fundamentals of Computer Architecture, Arithmetic Units Design, Bus protocols Architecture and Design, Memory Controllers Design and SoC Design
Other Skills and Qualities	<ul style="list-style-type: none">• Basic knowledge of C & Assembly level programming and scripting• Good debugging, problem solving/analytical skills• Good interpersonal skills and ability to work in a team
Mode of selection	Written Test and Interview
Monthly Consolidated salary	Rs. 31,000 /- onwards based on the post qualification relevant experience
Maximum Age	30 years