



Centre for Development of Advanced Computing (C-DAC)

**A Scientific Society of Ministry of Electronics & Information Technology (MeitY),
Government of India**

Vellayambalam, Thiruvananthapuram Kerala - 695033

Tel: 0471 2723333 (Ext: 249), Mobile: +91 98470 69184

www.cdac.in

Expression of Interest (EoI)

for

Licensing of

Processor, System and Peripheral IPs

EoI No: C-DAC(T)/EoI/HDG/2023/01

May 2023

Version 1 Rev 0

Contents

1	Notice Inviting EoI.....	3
2	Disclaimer	5
3	Introduction	6
4	Instruction to applicant's	11
5	EoI Evaluation	13
6	Standard Terms And Conditions.....	13
7	Annexure I.....	15
8	Frequently Asked Questions	20

1 NOTICE INVITING EOI

C-DAC Thiruvananthapuram, a Scientific Society under the Ministry of Electronics and Information Technology (MeitY), Govt. of India, invites expression of interest for *Licensing of Processor, System and Peripheral IPs*. C-DAC has developed a series of RISC-V based Single, Dual and Quad Core Processors under the brand VEGA Processors. C-DAC also has a wide variety of silicon proven System IPs and Peripheral IPs for SoC development. The details of VEGA Processors and the associated system and peripheral IP cores can be obtained from the website <https://vegaprocessors.in/>.

By licensing our IPs, your company can gain access to these technologies, accelerate your product development cycle, and reduce your time-to-market and cost of development. Also our team of experts will work closely with you to ensure seamless integration of our IPs into your products, providing technical support and assistance throughout the licensing process.

Firms may please submit their sealed EoI in the format mentioned in the Annexure I as email and subsequently hard copy in sealed envelope.

This document provides information to enable the partners to understand the broad requirements to submit their proposals expressing their interest for IP licensing.

Please Note: Since this is not a Request for Proposal (RFP), commercials are not required to be submitted at this stage.

The details are as below:

Issue of EoI	:	05/05/2023
Last date for receipt of queries	:	13/05/2023, 5:00 PM
Clarification to queries	:	18/05/2023, 5:00 PM
Last date for receipt of EoI	:	28/05/2023, 5:00 PM
Intimation regarding EoI qualification	:	Within ten days of receipt of application in C-DAC

Important Notes:

- The information to be furnished by the bidders is given in Annexure-I. Interested parties can submit the EoI in the application form in Annexure-I duly filled in with all relevant supporting documents as mentioned in section 4 of EoI document.
- The EoI can be submitted as email and subsequently as hard copy in sealed envelope, properly marked as “**Expression of Interest – Licensing of Processor, System and Peripheral IPs**” so as to reach the following address on or before **28th May 2023 by 5:00 PM**.

Head, Technology Promotion Centre

Centre for Development of Advanced Computing (C-DAC)

Vellayambalam, Thiruvananthapuram, Kerala, India, PIN: 695033

Phone: 0471 2723333 (Ext: 249), Mobile: +91 98470 69184

Email: tpc@cdac.in , Website: www.cdac.in

- C-DAC shall not be responsible for non-receipt / non-delivery or late receipt of the EoI documents due to any reason whatsoever.
- At any time of EoI, C-DAC may carry out amendment(s) to this EoI document and/or the schedule. The amendment will be made available on the website (www.cdac.in) and will be binding on them. The Authority may at its discretion extend the deadline for the submission of proposals.
- To assist in the examination, and evaluation of EoI, C-DAC, at its discretion can ask the bidder for the clarification of their EoI. The request for clarification and the response shall be in writing through email. However clarification at the initiative of the bidder shall not be entertained after submission of EoI.
- C-DAC, reserves the right to visit the facilities of the bidder if required. The official(s) of C-DAC may visit bidder's facilities for the assessment.
- Bidders, if they choose, may visit C-DAC with prior appointment with the contact person as stated below prior to submission of their Expression of Interest.
- C-DAC reserves the right to accept or reject any application without assigning any reason thereof.
- EoI that are incomplete in any respect or those that are not consistent with the requirements as specified in this document or those that do not adhere to formats, wherever specified may be considered non-responsive and may be liable for rejection and no further correspondences will be entertained with such bidders.
- Canvassing in any form would disqualify the applicant.
- The EoI document may be downloaded from the website, www.cdac.in

For any technical clarification, please approach the following person(s):-

Head, Technology Promotion Centre

Centre for Development of Advanced Computing (C-DAC)

Vellayambalam, Thiruvananthapuram, Kerala, India, PIN: 695033

Phone: 0471 2727508, Mobile: +91 98470 69184

Email: tpc@cdac.in, Website: www.cdac.in

Competent Authority
C-DAC Thiruvananthapuram

2 DISCLAIMER

1. Centre for Development of Advanced Computing, Thiruvananthapuram (herein after called C-DAC) has prepared this Expression of Interest Document (“EoI Document”) solely to invite prospective organizations for licensing of IPs. While C-DAC has taken due care in the preparation of information contained herein and believes it to be accurate, neither C-DAC or any of its Authorities or Agencies, nor any of their respective officers, employees, agents or advisors give any warranty or make any representations, express or implied as to the completeness or accuracy of the information contained in this document or any information which may be provided in association with it.
2. This information is not intended to be exhaustive and interested parties are required to make their own inquiries in order to submit the EoI. The information is provided on the basis that it is non-binding on C-DAC, any of its authorities or agencies or any of their respective officers, employees, agents or advisors.
3. C-DAC reserves the right not to proceed with the EoI process at any stage without assigning any reasons thereof, or to alter the time table reflected in this document or to change the process or procedure to be applied. It also reserves the right to decline to discuss the EoI further with any party submitting an EoI.
4. C-DAC will not be liable to pay / reimburse any cost / losses / expenses / penalties / damages of whatsoever nature to any person (s) / entity (ties) submitting the EoI.
5. This EoI Document is neither an agreement nor an offer and is only an invitation by C-DAC to the entities that are qualified to submit their Expression of Interest as stated in the Notice Inviting EoI.
6. The purpose of this Notice Inviting EoI Document is to provide interested applicant’s with information to assist the formulation of their ideas.
7. The issue of this EoI does not imply that C-DAC is bound to select an agency.

3 INTRODUCTION

Centre for Development of Advanced Computing (C-DAC) is the premier R&D organization of the Ministry of Electronics and Information Technology (MeitY), Govt. of India, for carrying out R&D in IT, Electronics and associated areas. Different areas of C-DAC, had originated at different times, many of which came out as a result of identification of opportunities. C-DAC has today emerged as a prominent R&D organization in IT&E (Information Technologies and Electronics) in the country working on strengthening national technological capabilities in the context of global developments in the field and responding to change in the market need in selected foundation areas.

C-DAC has expertise in the design of VLSI Systems, ASIC, IP development, microprocessor, microcontroller and DSP hardware and software technologies, which form the key building blocks in many industrial and strategic technology areas. C-DAC has designed, deployed and transferred manufacturing technology for numerous large scale electronics systems as well as small footprint VLSI and Embedded System Products for applications ranging from Supercomputing, Energy Measurement, Personal Computing, Medical appliances, Power and Process control, Wireless Broadband, Sonar and Acoustic detection, etc.

As part of the Digital India RISC-V (DIR-V) Program initiated and funded by the Ministry of Electronics and Information Technology (MeitY), Govt. of India, C-DAC has designed and developed indigenously, a family of Microprocessors, related IPs and the complete ecosystem that enables fully indigenous product development meeting various requirements in the strategic, industrial and commercial sectors. The DIR-V program has been conceived considering the growing need in India to attain self-sufficiency in many sectors, especially in the electronics sector. As an emerging super power, India is growing rapidly and automation and digitalization plays a vital role. The digital technologies are heavily dependent on electronic devices which are either imported or built on parts originating from foreign countries. The Microprocessors which form the core component of any electronic equipment are entirely imported from foreign countries.

As part of the Digital India RISC-V (DIR-V) Program, C-DAC has successfully completed the development of the VEGA series of microprocessors in soft IP form, viz. 32-bit single-core (in-order), 64-bit single-core (in-order & out-of-order), 64-bit dual-core (out-of-order), and 64-bit quad-core (out-of-order). These high performance processors are based on the open source RISC-V Instruction Set Architecture with Multilevel Caches, Memory Management Unit and Coherent Interconnect.

The System IPs and Peripheral IPs refer to intellectual property blocks that are designed to be integrated into a larger system-on-chip (SoC) design. These IPs are pre-designed, pre-verified, and pre-validated building blocks that can be quickly and easily integrated into an SoC, reducing design time and costs. System IPs include the core components of an SoC, such as memory controllers, bus interfaces, and system interconnects. Peripheral IPs, on the other hand, include components that interact with the outside world, such as communication interfaces, and peripheral controllers. C-DAC has a wide range of System and Peripheral IPs which are Silicon proven, consisting of the robust RTL, extensively verified and fully synthesizable technology independent IP cores.

Multiple SoCs based on the VEGA series of microprocessors are in advanced stages of ASIC implementation. C-DAC has already taped out an SoC based on 32-bit Single Core VEGA processor called THEJAS32. THEJAS64 SoC, designed and developed around the 64-bit single-core (in-order) VEGA processor, is taped out to the Semi-Conductor Laboratory (SCL) Chandigarh. A 64-bit dual core processor based SoC (DHRUV64) will be taped out soon followed by a 64-bit quad core processor based SoC (DHANUSH64). The SoCs will be realized, integrating in-house developed silicon proven peripheral IP's, targeting various applications in Strategic, Industrial, Automotive, Health, Consumer areas.

Objective: The objective of this EoI is to solicit proposals from the interested parties who wish to license indigenous VEGA Processor, System and Peripheral IPs to design and develop System-on-Chips (SoCs) to convert their existing product/solutions/ideas or to develop new product / solutions / ideas.

C-DAC invites Expression of Interest (EoI) from Indian companies / firms (Applicants) having experience in Design, Development & Commercialization of VLSI ASICs and System-on-Chips who wish to be considered for licensing of following IPs.

1. Processor IPs

1. VEGA ET1031 (32-bit single core 3-stage in-order RISC-V processor)
2. VEGA AS1061 (64-bit single core 6-stage in-order RISC-V processor)
3. VEGA AS1161 (64-bit single core 16-stage pipeline out-of-order RISC-V processor)
4. VEGA AS2161 (64-bit dual core 16-stage pipeline out-of-order RISC-V processor)
5. VEGA AS4161 (64-bit quad core 16-stage pipeline out-of-order RISC-V processor)

2. System IPs

1. ERPLIC - Interrupt Controller
2. ERDMA - Direct Memory Access Controller

3. Peripheral IPs

1. EROTG1 – USB2.0 On-the-Go Controller
2. ERUSBHC – USB2.0 Host Controller
3. ERUSB2 – USB2.0 Function Controller
4. ERMAC - Ethernet Media Access Controller (10/100 Mbps)
5. ERGMAC - Gigabit Ethernet Media Access Controller
6. ERRTC – Real Time Clock
7. ERTIMER - Configurable Timer
8. ERUART – UART
9. ERSPI - SPI Controller
10. ERI2C - I2C Controller
11. ERPWM - PWM Controller
12. ERGPIO - GPIO Controller
13. ERSDHOST - SD Host Controller
14. ERQSPI - QSPI Controller
15. ERSMC - Static Memory Controller (SRAM/NOR)

C-DAC is seeking to short list interested firms in the licensing of its Processor, System and Peripheral IPs. These IP blocks can be integrated into existing products or solutions or be used for the development of new products or solutions. The licensing of C-DAC's IP can provide firms with access to innovative technologies, reducing design costs and accelerating product development time. C-DAC will work with these firms to provide technical support and guidance throughout the licensing process, ensuring seamless integration of the IP into their product.

The objective of this request for EoI is to identify eligible applicants who are interested in licensing the *Processor, System and Peripheral IPs* designed and developed by C-DAC.

In view of this, Expression of Interest (EoI) is invited as email and hard copy in sealed envelope superscribed as **<Expression of Interest – Licensing of Processor, System and Peripheral IPs >**.

1. From the applicants who meet the eligibility criteria as set out in Section 4.1
2. Agree to abide by the terms and conditions contained in this request for EoI document without any condition.

3.1 WHO CAN APPLY

Companies (including Private Limited Companies, PSUs, MSMEs and start-ups), R&D institutions (comprising academia, research institutes, R&D organizations and companies), firm, partnership firms working in niche technologies in India and having requisite expertise in R&D / contract research registered under the Companies Act, 2013/the Partnership Act, 1932, Limited Liability Partnership Act, 2008 and Autonomous body, Education/Research institution, duly approved Trust registered under Trusts Act, and Society registered under Societies Registration Act, 1950 and who have their registered offices in India.

3.2 SCHEDULE FOR EOI

The details for submitting the EoI are furnished below.

Table 1 Details for submitting EoI

1	Name and Address for sending hard copy	Head, Technology Promotion Centre Centre for Development of Advanced Computing (C-DAC) Vellayambalam, Thiruvananthapuram, Kerala, India, PIN: 695033 Phone: 0471 2723333 (Ext: 249), Mobile: +91 98470 69184 Email: tpc@cdac.in , Website: www.cdac.in
2	Email for soft copy submission	tpc@cdac.in
3	Closing Date	28/05/2023 5:00 PM
3	Name of the Contact Person for clarification	Head, Technology Promotion Centre Centre for Development of Advanced Computing (C-DAC) Vellayambalam, Thiruvananthapuram, Kerala, India, PIN: 695033 Phone: 0471 2723333 (Ext: 249), Mobile: +91 98470 69184 Email: tpc@cdac.in Website: www.cdac.in

3.3 MODES OF ENGAGEMENT

In order to meet the stated objective, C-DAC invites EoI from the Companies (including private limited companies, PSUs, MSMEs and start-ups), R&D institutions (comprising academia, research institutes, R&D organizations and companies), Firms, Partnership Firms, Trusts, Societies working in niche technologies in India and having requisite expertise in R&D / contract research for licensing of IP's as detailed below in the following modes subject to GFR 2017 and CVC guidelines.

3.3.1 IP Licensing

C-DAC's IPs will be licensed in Non-exclusive License mode where the Licensee has the right to use the intellectual property, without restricting C-DAC from using the IP themselves or granting licenses to other parties. Licensee shall not sublicense the licensed IP to any person or entity.

3.3.1.1 Deliverables

1. IP specification
2. RTL Source code
3. Test Benches
4. User guide
5. Integration guidelines

3.4 ROLES OF LICENSEE

3.4.1 IP Licensing

1. The Licensee firm has to license the IP considering their requirements.
2. The Licensee firm needs to sign an NDA and MoA.
3. The Licensee firm is responsible for paying the licensing fees agreed upon in the MoA.
4. The Licensee firm is responsible for the development and commercialization of the product or technology by incorporating the licensed IP.
5. The Licensee firm is required to take measures to protect the licensor's IP, by implementing security measures, maintaining confidentiality, and preventing unauthorized use or disclosure of the IP.
6. The Licensee firm is required to provide periodic usage reports of the licensed IP.

3.5 ROLES OF C-DAC

3.5.1 IP Licensing

1. C-DAC is responsible for granting the Licensee the rights to use the licensed IP according to the terms of the MoA.
2. C-DAC will provide technical support to the Licensee for IPs licensed as per the terms and conditions agreed upon.

3.6 APPLICANT'S ELIGIBILITY CRITERIA

This process is open to all applicants who fulfil the Qualification criteria as set out in Section 4.1 of this document. The applicants should furnish information on the lines of Section 4.1 in their EoI proposal.

4 INSTRUCTION TO APPLICANTS

The EoI is to be submitted in the manner prescribed below. All information as detailed below is to be submitted in soft copy through email and hard copy in sealed envelope.

1. Documents to be furnished
 - a. Applicant's Expression of Interest (Format - 1)
 - b. Application Form (Format - 2)
 - c. Experience of the Organization (Format - 3)
 - d. Financial Strength of the Organization (Format - 4)
 - e. Declaration (Format - 5)
2. Important Notes
 - a. Queries, if any, may be referred in writing to **tpc@cdac.in** which will be addressed via email.
 - b. All Licensees are expected to exercise utmost caution while dealing with prospective clients, so as not to misrepresent facts or give any inappropriate assurances on behalf of C-DAC.
 - c. A Non-Disclosure Agreement (NDA) will be signed prior to initiating technical deliberations with eligible applicants.
 - d. C-DAC will provide the terms and conditions for the IP licensing to the qualified applicants. The eligible applicants on agreeing to the terms and conditions for the IP licensing need to sign a MoA for obtaining the IP.
 - e. The MoA signing needs to be done within three months of C-DAC providing the terms and conditions for the IP licensing.
 - f. At any time prior to the deadline for submission of EoIs, C-DAC may modify any part of this document. Such change(s) if any may be in the form of an addendum / corrigendum and will be uploaded in C-DAC website <https://www.cdac.in>. All such change(s) will automatically become part of this EoI and binding on all applicants. Interested applicants are advised to regularly refer the C-DAC's URLs referred above.
 - g. The applicants may submit the EoI as per EoI Schedule. C-DAC may ask applicants for clarifications or additional documents / credentials at its discretion. Clarifications (if any) will be e-mailed to the applicants.
 - h. The applicant should have the necessary credentials and be suitably equipped to execute the activities envisaged in the indicative roles and responsibilities listed out.
 - i. The prime responsibility lies with applicant associating with C-DAC as the work / activities can't be fully or partially sub-let to any other entity without prior approval and written consent of C-DAC.

4.1 QUALIFICATION CRITERIA

The qualification criteria for the EoI are listed below.

Table 2 Qualification Criteria

SI No	Pre-qualification criteria	Supporting Documents
1.	Applicant shall be a firm / company / partnership firm / trust / society / autonomous body registered under the Companies Act, 2013 / the partnership Act, 1932, Limited Liability Partnership Act, 2008 or any other relevant Law, and who have their registered offices in India.	Copy of certificate of incorporation, registration, partnership deed.
2.	The applicant must have experience in Design & Development of electronics products especially, embedded, FPGA / CPLD/ASIC technologies. Applicant shall detail their experience in other field as well *	Format- 3 Certificate by Authorized Signatory of the applicant or Company Secretary of the firm/organization and other similar documents.
3.	Applicant has to submit audited annual accounts statement of the last three (03) consecutive financial years *	Format - 4 to be certified & validated by Chartered Accountant (CA) of the applicant with audited annual accounts.
4.	The applicant must not be blacklisted or debarred by any Central Govt. / State Govt. / PSU / Municipal Corporations / other Govt. Bodies, as on date of submission of proposals.	Certificate signed by the Authorized Signatory.
5.	PAN, TAN, GST Registration Certificate.	Copy of Certificate to be enclosed.

* Start-ups recognized by DIPP will be given waiver

Note: The applicants should provide all the necessary documents as listed in table above. C-DAC reserves the right to reject any proposal not fulfilling the eligibility criteria. If in the view of applicant, any exemption / relaxation are applicable to them from any of the eligibility requirements, under any rules / process / guidelines / directives of Government of India, applicant may submit their claim for the applicable exemption / relaxation, quoting the valid rule / process / guidelines / directives. In this case the applicant must submit necessary and sufficient documents along with the EoI, in support of their claim. The decision about granting the exemption/relaxation will be taken by the evaluation committee which is empowered to grant exemption / relaxation. The relevant and valid certificates in support of claim of exemption must be submitted as soft and hard copy.

5 EOI EVALUATION

1. Screening of EoI shall be carried out by the evaluation committee as per qualification conditions mentioned in this document.
2. Eligible applicants will be informed about further steps on license processing.
3. C-DAC reserves the sole right for selection of Licensees for licensing its IPs.

6 STANDARD TERMS AND CONDITIONS

6.1 GENERAL TERMS AND CONDITIONS

1. Information shared with the applicant / Licensee cannot be shared with the third party unless agreed in writing by C-DAC.

2. Contract/Agreement

C-DAC will sign a MoA with Licensee. The Licensee is required to comply with the terms and conditions which will be part of the MoA.

C-DAC reserves the right to enter into the MoA with more than one Licensee.

3. Right to Amend/Cancel

- a. C-DAC reserves the right to amend the eligibility criteria, scope of supply, technical specifications etc.

- b. C-DAC reserves the right to cancel the entire EoI without assigning any reasons thereof.

4. Penalty for Use of Undue Influence

The applicant undertakes that he has not given, offered or promised to give, directly or indirectly, any gift, consideration, reward, commission, fees, brokerage or inducement to any person in service of C-DAC or otherwise in procuring the contract / agreement or forbearing to do or for having done or forborne to do any act in relation to the obtaining or execution of the contract / agreement or any other contract / agreement with the Government of India for showing or forbearing to show favour or disfavour to any person in relation to the Contract/Agreement or any other contract/agreement with the Government of India. Any breach of the aforesaid undertaking by the applicant or anyone employed by him or acting on his behalf (whether with or without the knowledge of the applicant) or the commission of any offence by the applicant or anyone employed by him or acting on his behalf, as defined in Chapter 9 of the Indian Penal Code, 1860 or the Prevention of Corruption Act 1986 or any other Act enacted for the prevention of corruption shall entitle C-DAC to cancel the contract / agreement and all or other contract / agreements with the applicant and recover from the

applicant the amount of any loss arising from such cancellation. A decision of C-DAC to the effect that a breach of the undertaking had been committed shall be final and binding on the applicant, giving or offering of any gift, bribe or inducement or any attempt at any such act behalf of the applicant towards any officer / employee of C-DAC or to any other person in a position to influence any officer / employee of C-DAC for showing any favour in relation to this or any other contract / agreement, shall render the applicant to such liability / penalty as C-DAC may deem proper, including but not limited to termination of the contract / agreement, imposition of penal damages, and refund of the amount paid by C-DAC.

5. Intellectual Property Rights (IPR)

C-DAC holds the Intellectual Property Rights (IPRs) pertaining to the R & D in these areas. C-DAC reserves the right to hold these IPRs with them. The use of these IPRs will be allowed only for the purpose stipulated in the agreement to be signed for licensing activity.

6. Corrupt or Fraudulent Practices

- a. It is expected that the applicants who wish to license the IPs have highest standards of ethics.
- b. C-DAC will reject the application if it encounters that the applicant has engaged in corrupt or fraudulent practices.

7. Confidentiality

The applicant / or their personnel shall not, either during the term or after expiration of licensing period under this EoI, disclose any proprietary or confidential information relating to the services, contract or C-DAC's business or operations without the prior written consent of C-DAC. The applicant and / or their personnel shall not, either during the term or after expiration of this contract, undertake any public communication, press / media releases relating to the services, contract, or C-DAC's business or operations without the written consent of C-DAC. Information available on public domain is excluded from confidentiality clause. The eligible applicants will be required to sign a Non-Disclosure Agreement (NDA) with C-DAC towards the confidentiality requirements.

8. Interpretation of the Clauses in the EoI Document

In case of any ambiguity/dispute in the interpretation of any of the clauses in the EoI document, the interpretation of the clauses by Executive Director, C-DAC Thiruvananthapuram shall be final and binding on all parties.

7 ANNEXURE I

FORMATS FOR SUBMISSION APPLICANT'S EoI (FORMAT - 1)

To
The Executive Director
C-DAC, Vellayambalam
Trivandrum – 695033

Subject: Submission of EoI for Licensing of Processor IP's, System IP's and Peripheral IP's

Dear Sir,

In response to the Invitation for Expression of Interest (EoI) published on C-DAC website for the above purpose, we would like to express interest for the same.

As instructed, we attach one set of the following documents in a sealed envelope and soft copy through mail:

1. Application Form (Format - 2)
2. Experience in related fields (Format - 3)
3. Financial strength of the organization (Format - 4)
4. Declaration (Format - 5)

Sincerely Yours,

Signature of the Competent Authority (with date)

(full name)

(designation)

Stamp & Date

Encl.: As above.

Note

This is to be furnished on the letter head of the organization.

APPLICATION FORM (FORMAT - 2)

SL No	Organizational Details	
1	Name of the organization / year of establishment	
2	Main areas of work	
3	Type of organization firm / company / partnership firm registered under the Indian Companies Act, 1956 / the partnership Act, 1932 etc.	
4	Whether the Applicant has been blacklisted by any Central Govt. / State Govt. / PSU / Govt. Bodies / Autonomous / Anywhere else? If yes, details thereof.	
5	Address of registered office with telephone no. & fax	
6	Name of the Contact Person with Telephone No. Mobile No. Email Id.	
7	Organization chart	
8	Registration copy with any Govt. / Govt. undertaking Companies (if any)	
9	Accreditation Details ISO / CMM	
IP Licensing		
10	Interested IPs	1) Processor IPs a) b) 2) System IPs a) b) 3) Peripheral IPs a) b)
11	Preferred Mode of Licensing - MPW	1) Upfront Fee (one time)
12	Preferred Mode of Licensing -Production	1) Upfront Fee (one time) 2) Royalty 3) Upfront Fee + Royalty
13	Expression of Interest: Spell out the extent of interest	

Enclose:

1. Copy of Certificate of Incorporation
2. Copy of Article of Association in respect of #3 above.
3. Undertaking in respect of #4 above.

Signature of the Applicant / Authorized Signatory

(Full name of the Applicant / Authorized Signatory)

Stamp & Date

EXPERIENCE OF THE ORGANIZATION (FORMAT – 3)

ORGANIZATION EXPERIENCE

Experience in implementation of solutions in the area of electronics products especially, FPGA / CPLD/ASIC technologies				
Sl. No.	Items	Vertical/Area of Work	Number of assignments during last five (05) years	Order Value of each assignment in Lakhs of INR (enclose copy of each order)
1.	Experience of assignments in relevant areas mentioned above			
2.	Experience in carrying out assignments in relevant areas mentioned above in Government / public sector			

Signature of the Applicant / Authorized Signatory

(Full name of the Applicant / Authorized Signatory)

Stamp & Date

FINANCIAL STRENGTH OF THE ORGANIZATION (FORMAT – 4)

FINANCIAL STRENGTH OF THE ORGANIZATION

Sl. No.	Financial Year	Annual net profit	Overall annual turnover
1.			
2.			
3.			

Note: Please enclose auditor's certificate in support of your claim for last three years.

Signature of the Applicant / Authorized Signatory

(Full name of the Applicant / Authorized Signatory)

Stamp & Date

DECLARATION (FORMAT – 5)

DECLARATION

We hereby confirm that we are interested in *‘Licensing of Processor, System and Peripheral IPs* for SoC and Product development as per your EoI. All the information provided herewith is genuine and accurate.

(Authorized Person’s Signature)

Name: _____

Designation: _____

Date of Signature: _____

Stamp & Date

Note: The declaration is to be furnished on the letter head of the organization.

8 FREQUENTLY ASKED QUESTIONS

8.1 NON-TECHNICAL QUERIES

Sl No	Non-Technical Queries	Clarification
1	Is there any kind of Non-Disclosure agreement to be signed with C-DAC?	NDA will be signed with the selected bidders.
2	IP price and Licensing modes.	The IP price and licensing modes will be fixed by C-DAC and informed to the eligible applicants.
3	Is there any minimum price expected?	Currently this is an expression of interest, where the applicants are asked to submit EoIs. Once the EoIs are screened, the eligible applicants will be informed of price and licensing modes.
4	We are a start-up company. Is the ISO Certificate mandatory.	The EoI asks for the status of ISO Certifications. EoI from bidders without ISO certification also will be considered.

8.2 TECHNICAL QUERIES

Sl No	Technical Queries	Clarification
1	Are the IPs targeted for any specific technology node?	Presently, the IPs are not targeted for any specified technology node. Since the design is in RTL, it can be taken further in ASIC flow targeting any technology node.
2	Are the IPs taken through Physical Design flow?	Yes
3	How these IPs are verified on FPGA?	The IP cores have been integrated as a peripheral with the VEGA Processor core in a SoC framework.
4	What will be the procedure for Demo in lab?	Yes. Demo can be arranged based on request via e-mail.
5	Is the design FPGA specific?	No, the designs do not use FPGA specific primitives. The developed IP cores are RTL codes.