

Category No.3: C-DAC, Bangalore, Project Engineer – VLSI Architecture / VLSI Circuit / VLSI System / Microelectronic Circuit Design

Post:	Project Engineer – VLSI Architecture / VLSI Circuit / VLSI System / Microelectronic Circuit Design
Post Code:	PE-02/ACSMCD
No. of Posts	1
Duration of the position	Three Years
Educational Qualification	<ul style="list-style-type: none"> • Full time 1st Class B.E/ B. Tech in Electronics & Communication / Telecommunication / Electronics or Equivalent <li style="text-align: center;">AND • Full time 1st class M.E/M. Tech in Micro-Electronics/ VLSI Design/ Electronics or Equivalent <li style="text-align: center;">AND • Full time PhD in the topics of VLSI Architecture / VLSI circuit design / VLSI System Design / Microelectronic Circuit Design. <p>Candidates who have been awarded PhD degree are ONLY eligible to apply.</p>
Mode of selection	Written Test &/ Interview
Monthly Consolidated salary	Rs 60,000 plus based on the post qualification relevant experience
Maximum Age	32 years